

Fig.1

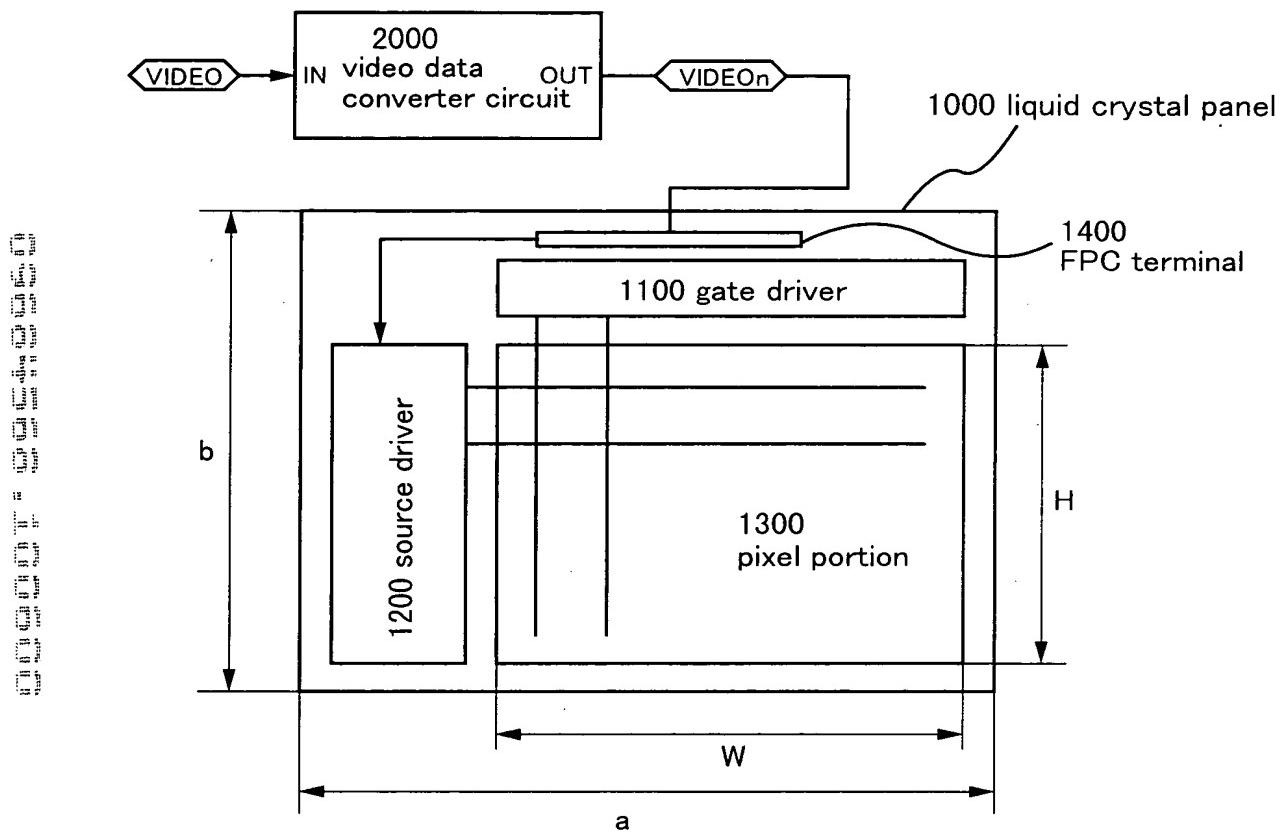


Fig.2

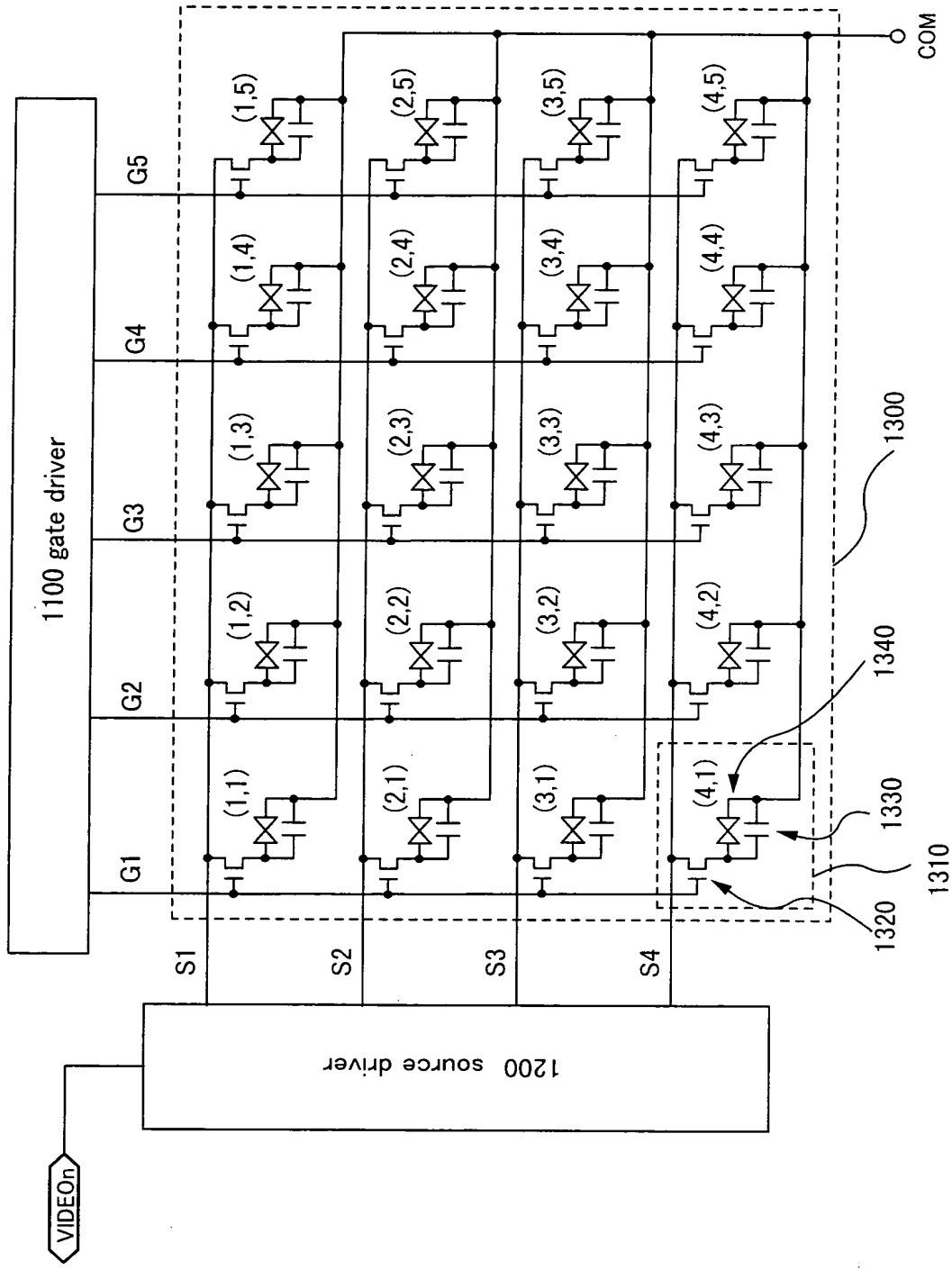


Fig.3

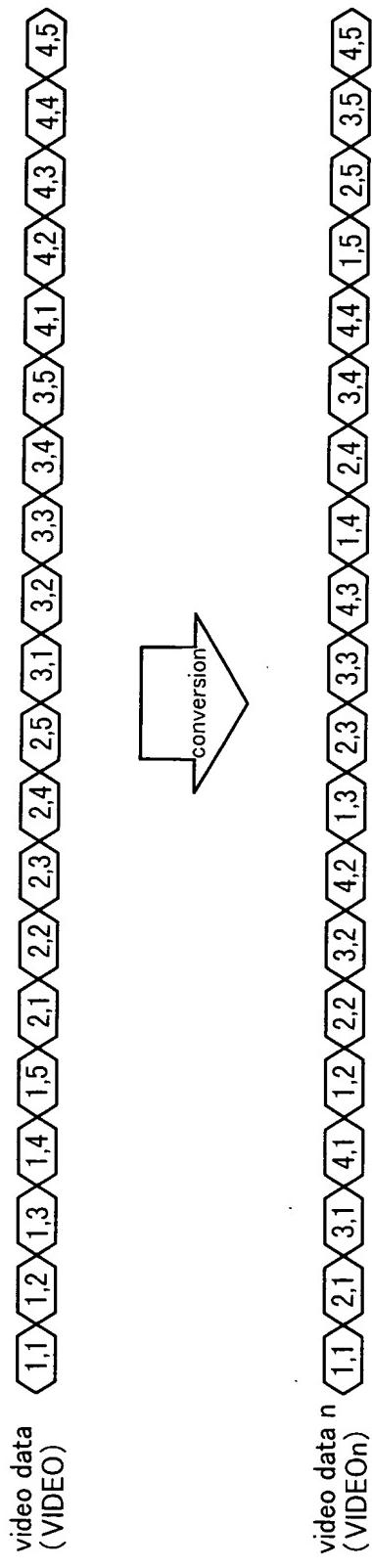


Fig. 4
Block diagram of a video converter circuit

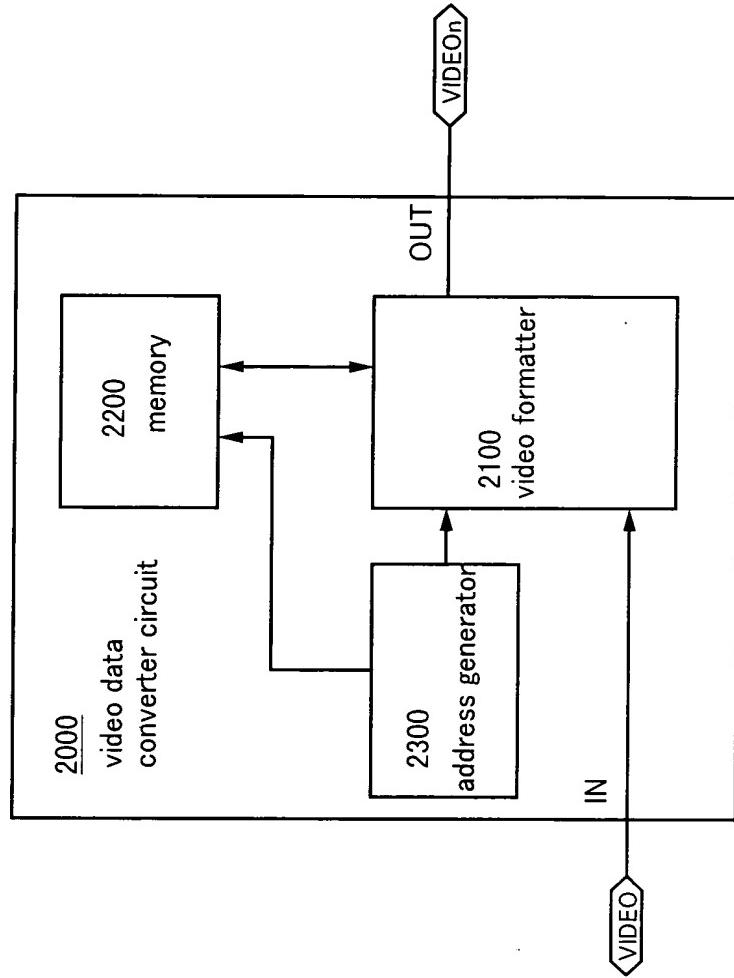


Fig.5

VIDEO On

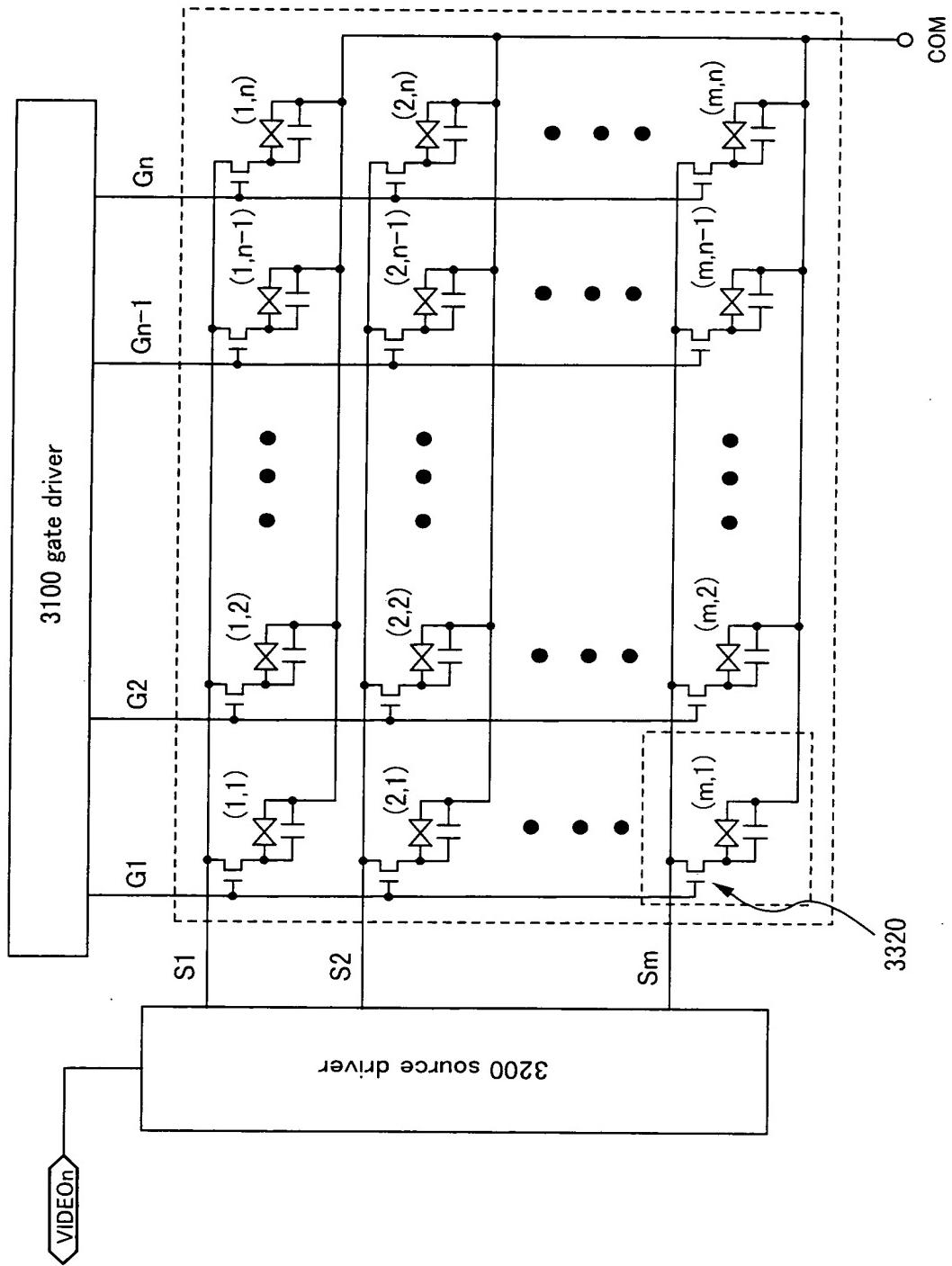


Fig.6

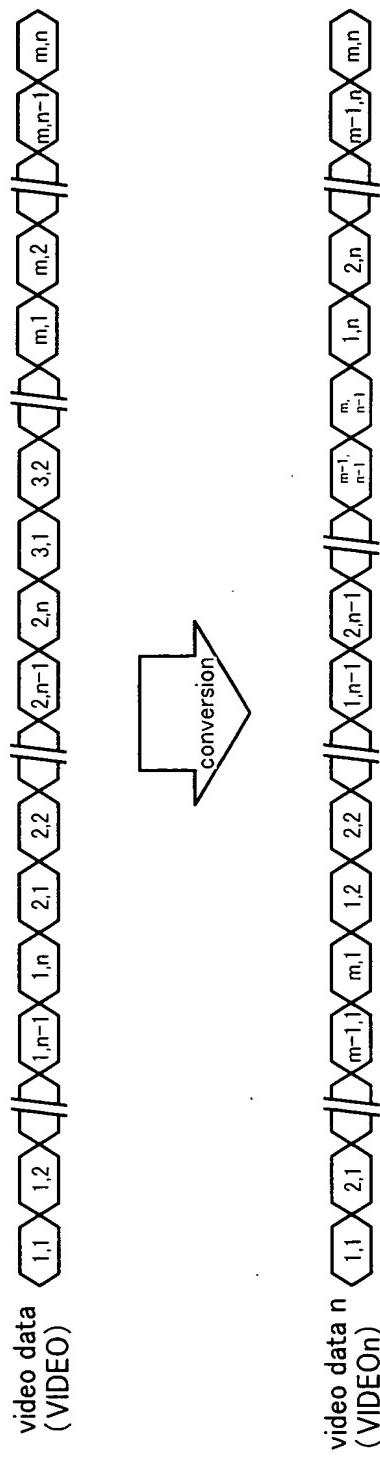
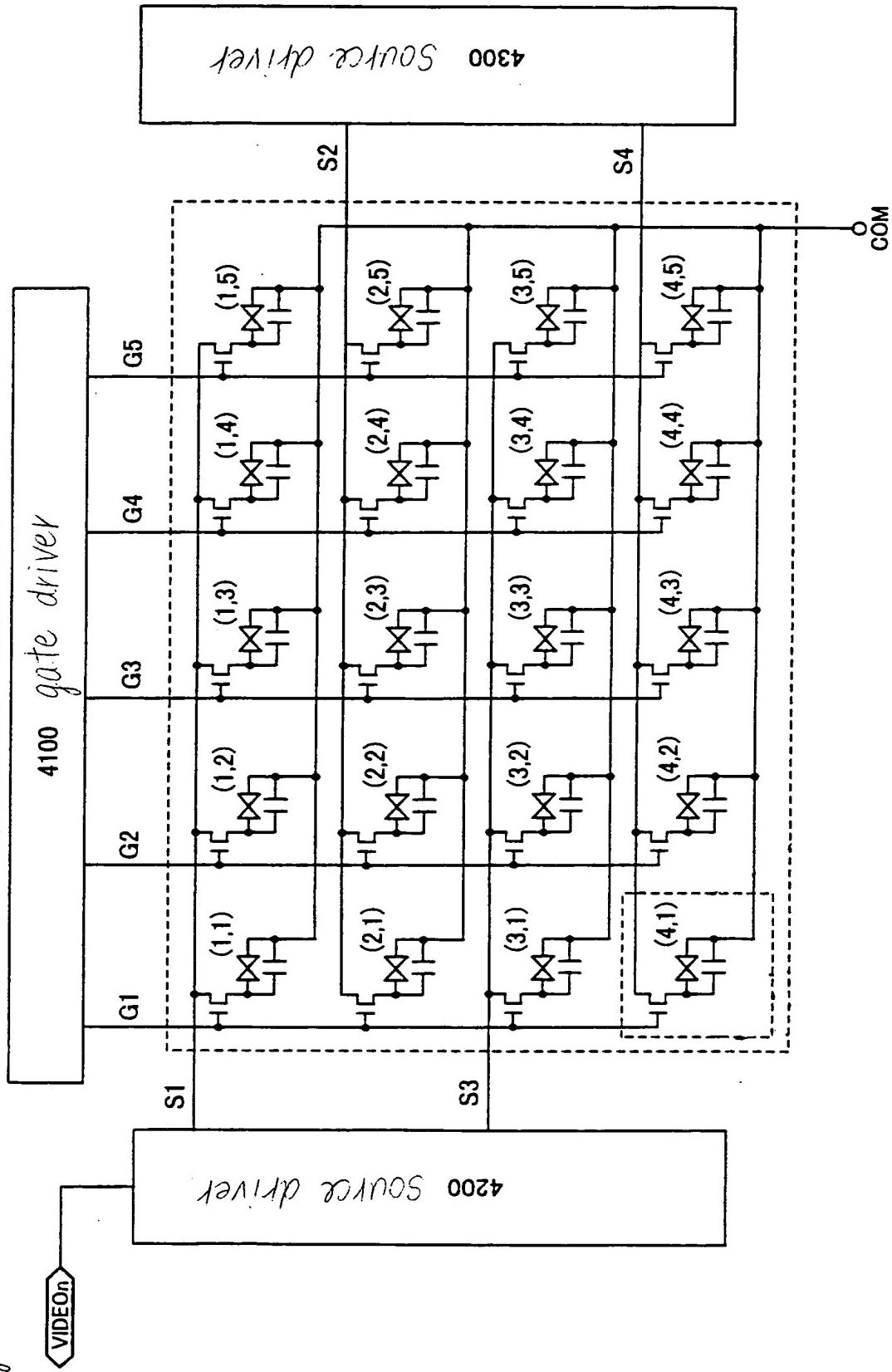


Fig. 7



6001 6002 6003a
6003b

Fig.8A

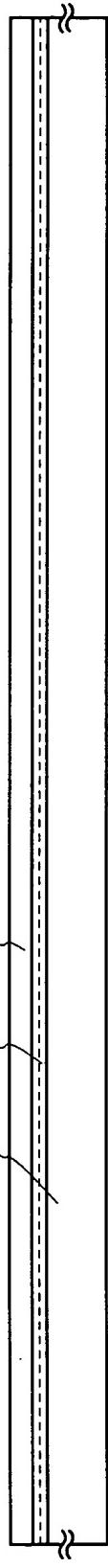


Fig.8B

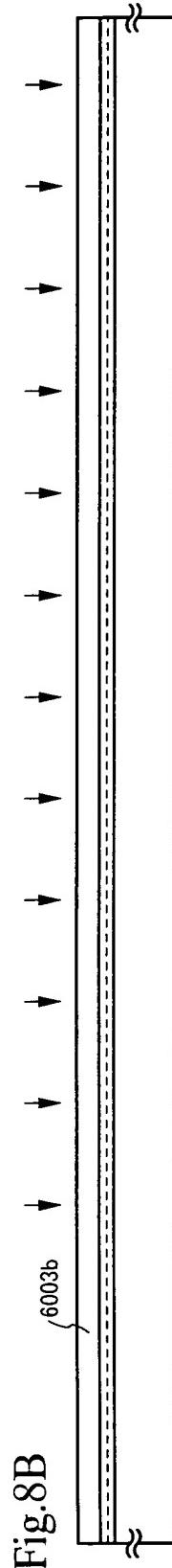


Fig.8C

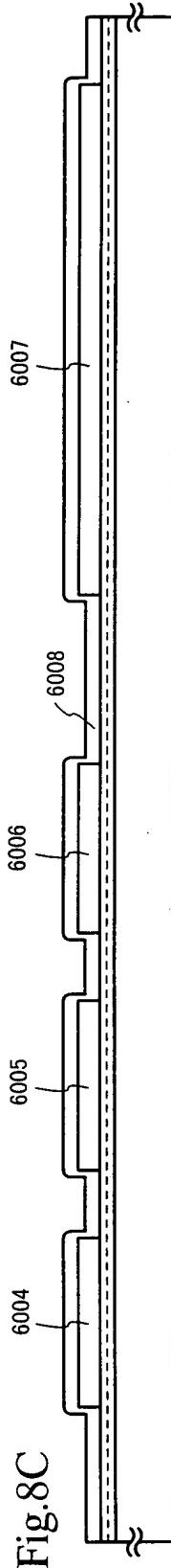
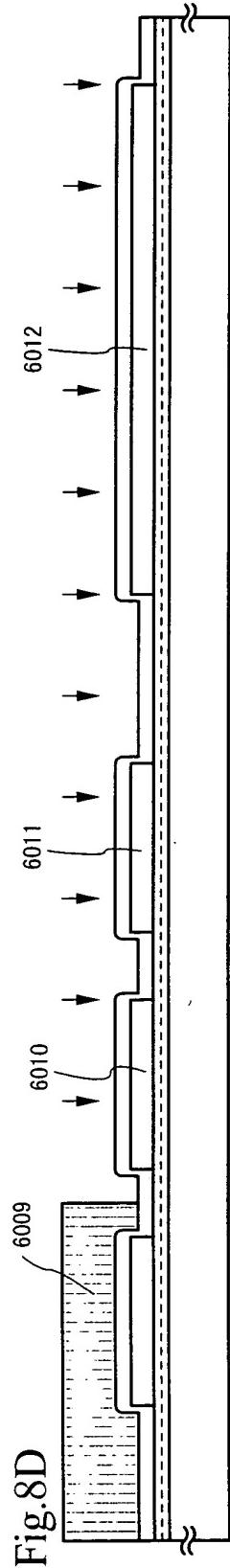


Fig.8D



6009 6010 6011

6006 6008

6007

Fig.9A

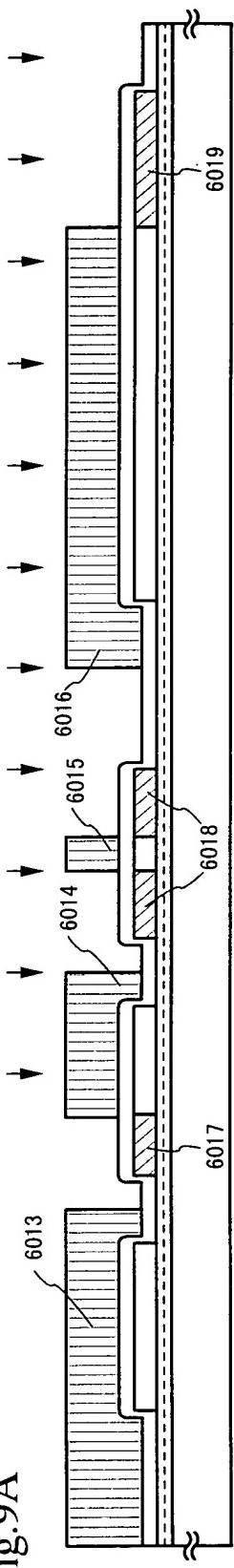


Fig.9B

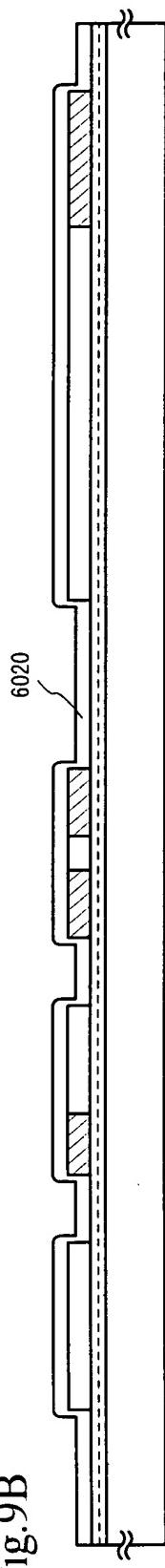


Fig.9C

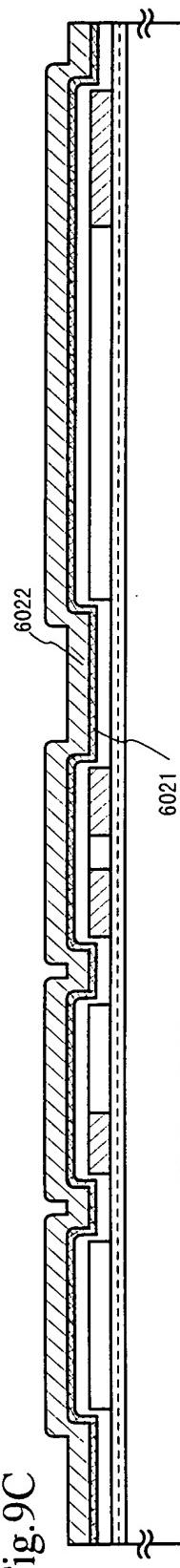


Fig.9D

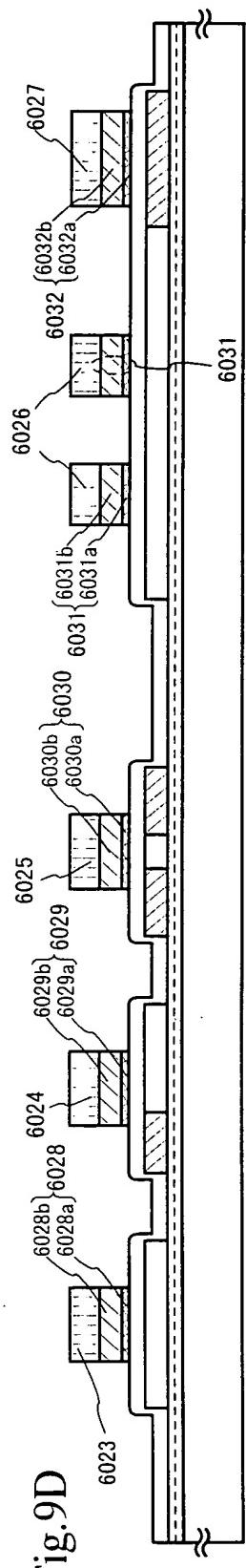


Fig.10A

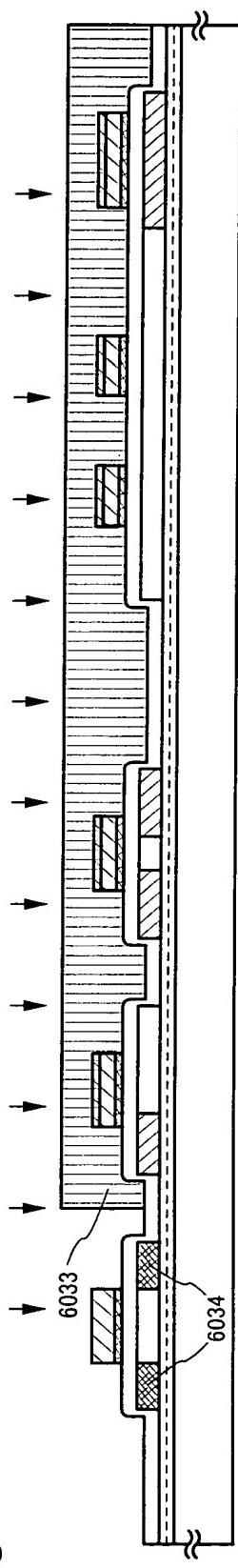


Fig.10B

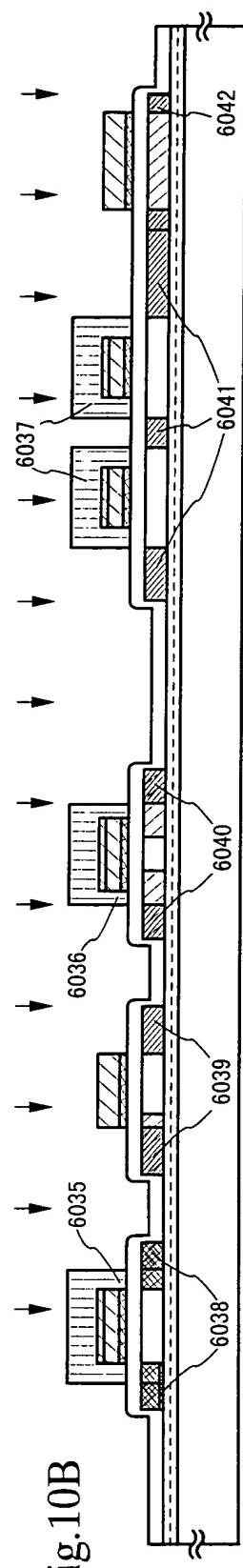


Fig.10C

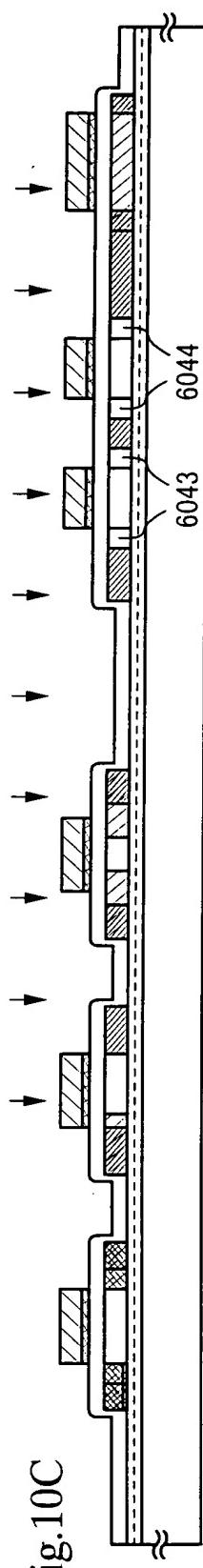
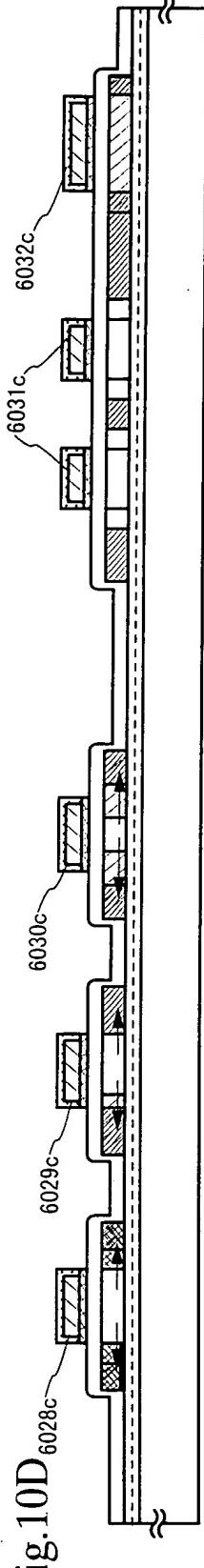


Fig.10D
6028c



6045 6046 6050 6051 6047 6048 6052 6049 6053 6054

Fig.11A

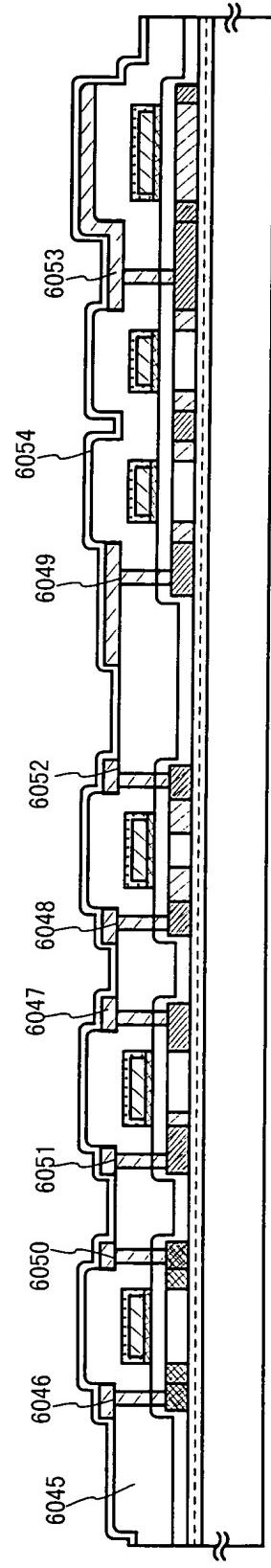


Fig.11B

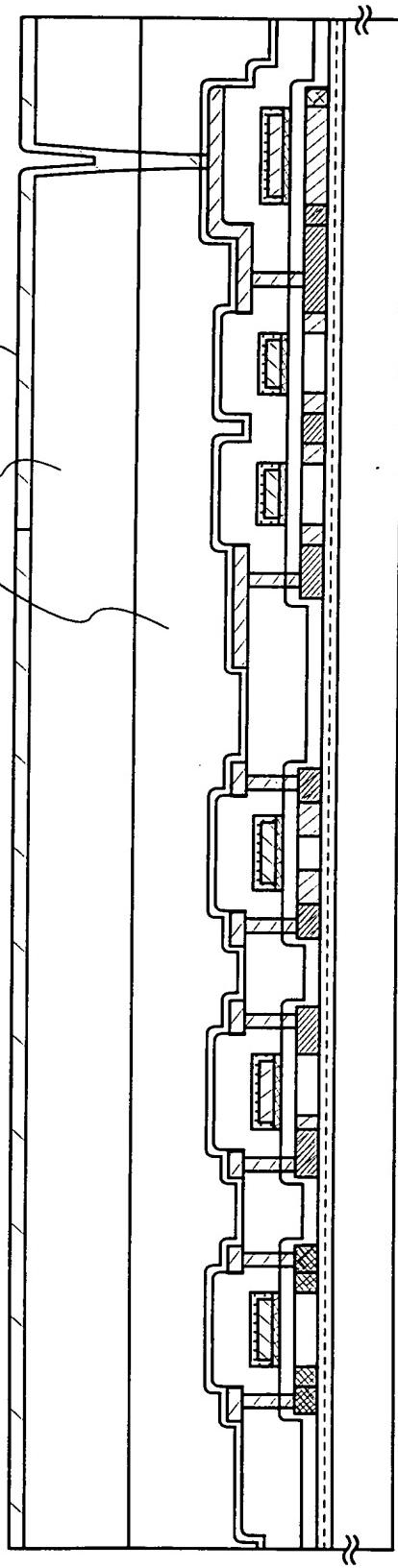


Fig.12

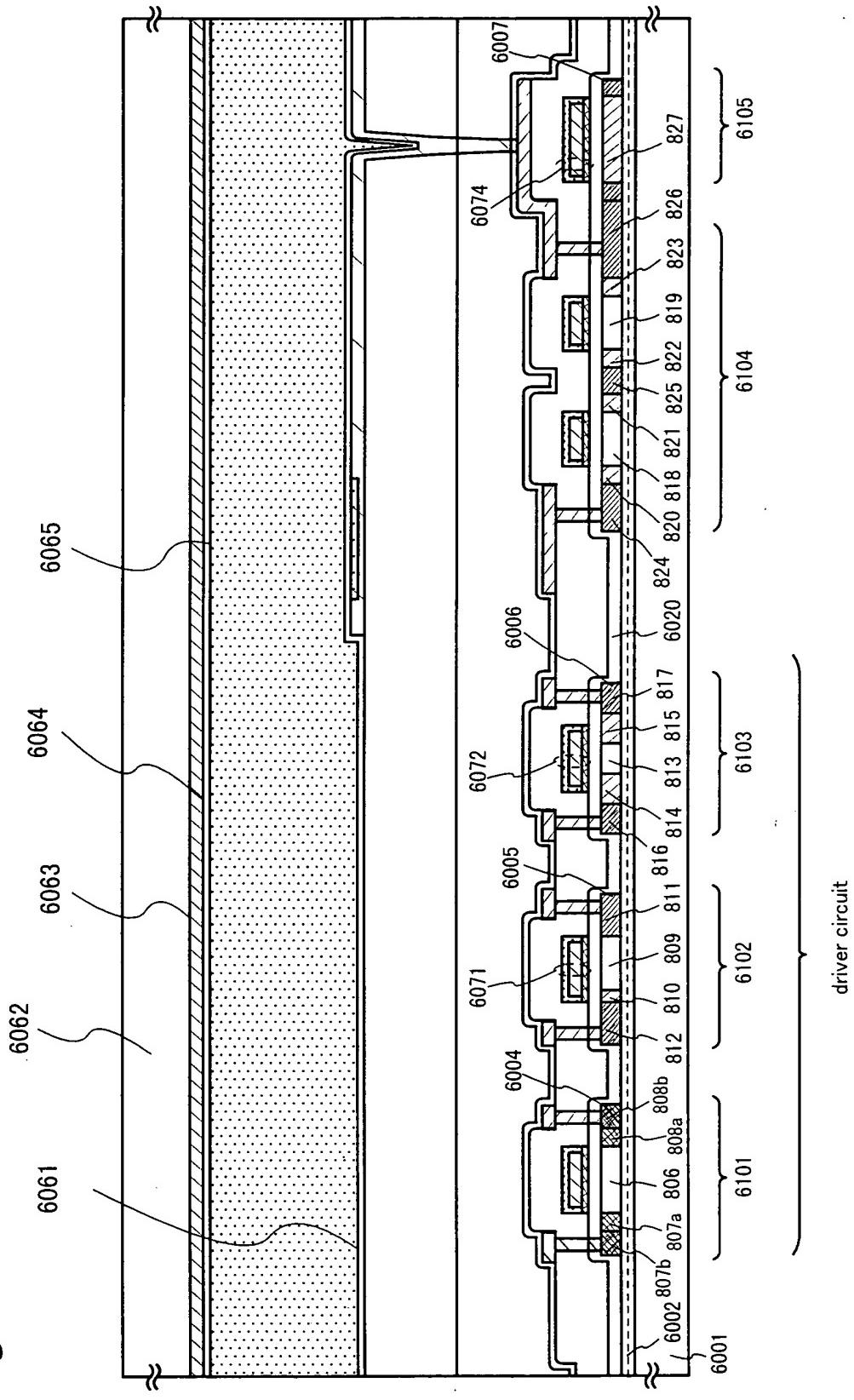
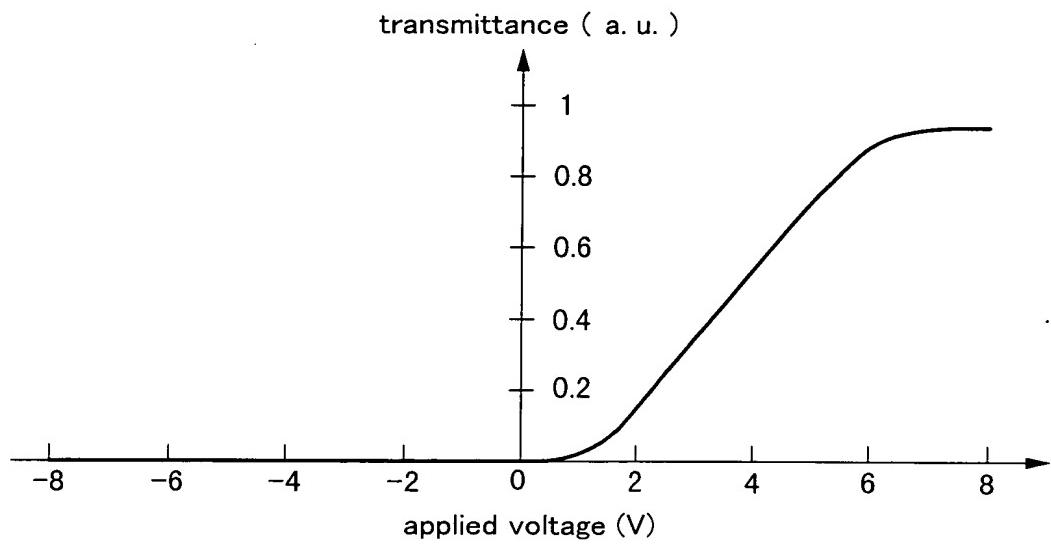


Fig.13



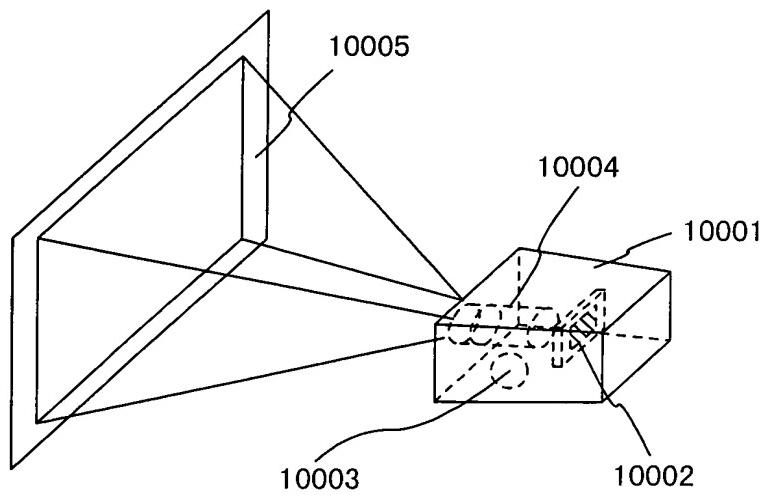


Fig.14A

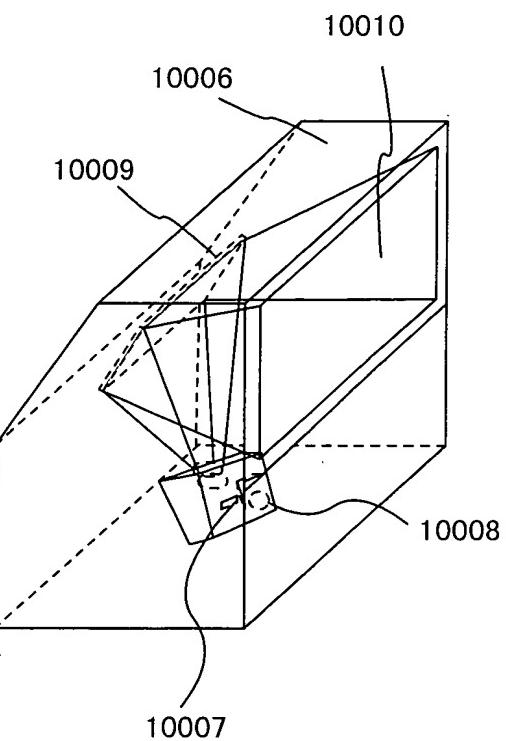


Fig.14B

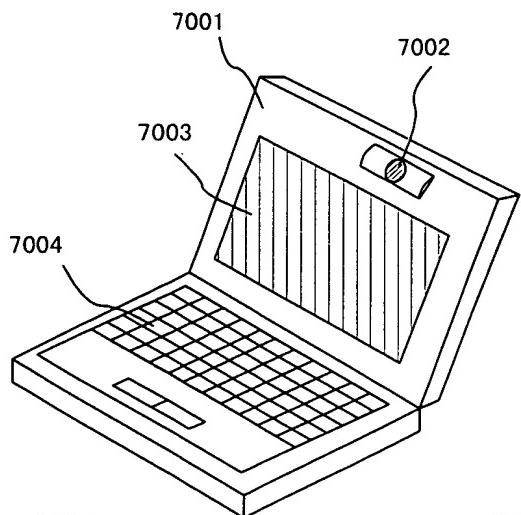


Fig.15A

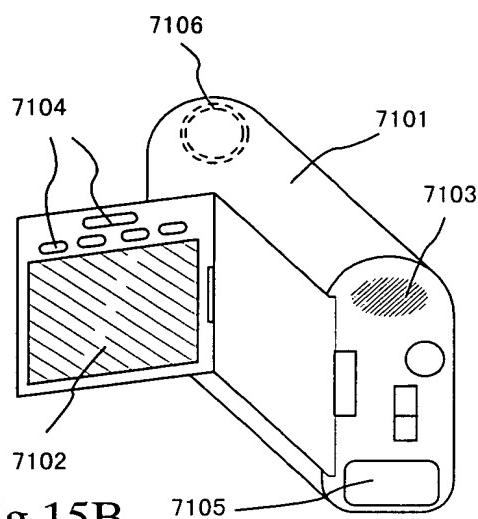


Fig.15B

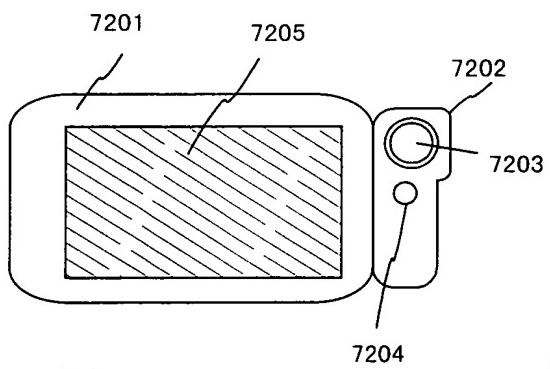


Fig.15C

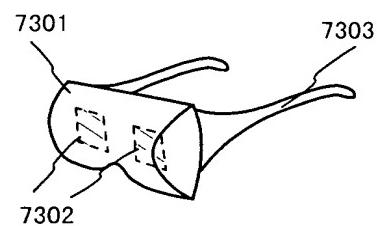


Fig.15D

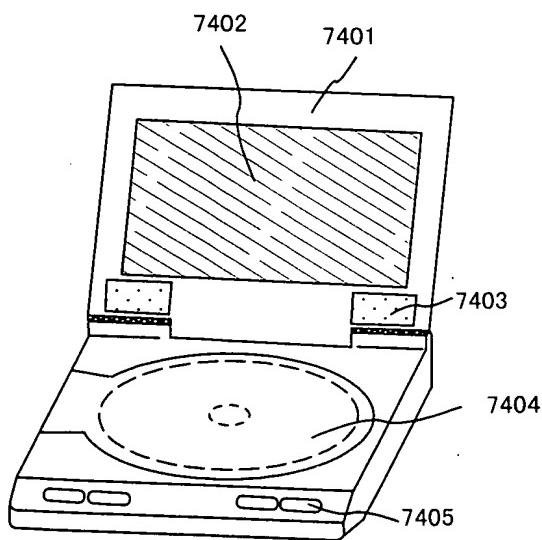


Fig.15E

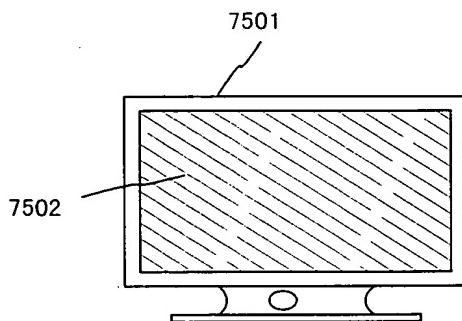


Fig.15F

Fig.16

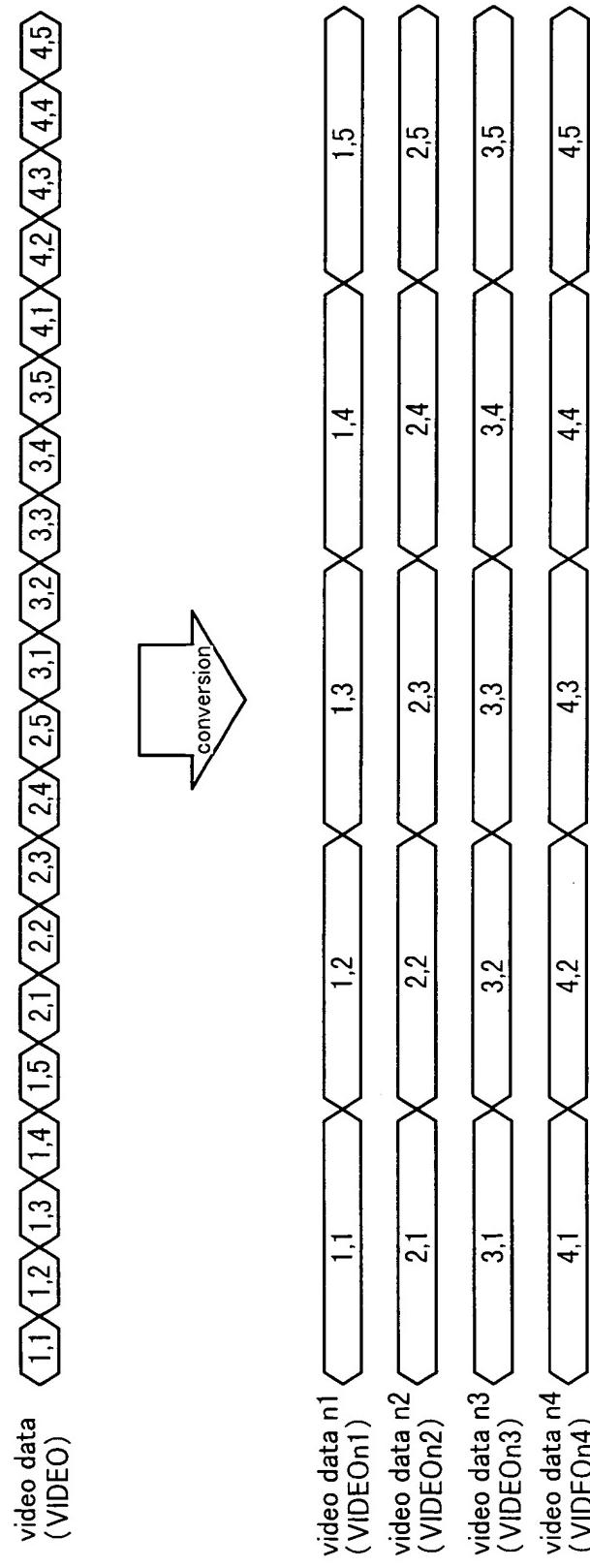


Fig.17

